Reg. No.											
----------	--	--	--	--	--	--	--	--	--	--	--



SEVENTH SEMESTER B.TECH. (INSTRUMENTATION AND CONTROL ENGG.) END SEMESTER EXAMINATIONS, NOV/DEC 2016

SUBJECT: REAL TIME EMBEDDED SYSTEM [ICE 443]

Time: 3 Hours MAX. MARKS: 50

Instructions to Candidates:

- **❖** Answer **ANY FIVE FULL** questions.
- Missing data may be suitably assumed.

1A.	Explain the working & key features of C55x processor.	4
1B.	With an application explain how multi-processor SOC works.	3
1C.	With timing diagram, briefly explain SRAM read operation.	3
2A.	Explain Direct mapped cache and set associate cache with examples.	4
2B.	What is cache? Explain how data flows from cache to processor and memory and vice versa?	4
2C.	List the drawback in Bluetooth protocol.	2
3A.	How page fault occurs? Explain how page fault will be serviced?	4
3B.	Explain the execution of READ operation in DRAM with timing diagram and timing	4
	parameters	
3C.	List all OSI layers and explain about presentation layer	2
4A.	Why Hotspot occurs? What are the power saving modes that are implemented in general	4
	embedded system setup? Explain with the help of a state machine diagram.	
4B.	Explain with an example the role of cache in memory organization and explain how data	5
	transfer occurs in it.	
4C.	Write the expression for power consumption in CMOS (ignoring the leakage)	1
5A.	With case study explain the working of inter integrated circuit communication protocol.	4
5B.	Explain the following. 1) Ethernet Mac algorithm 2) CSMA/CD with state machine diagram.	4
5C.	List the difference between Zigbee and Bluetooth protocol	2
6A.	Explain the working of IEEE 802. 11 protocol	5
6B.	Illustrate the working of Bluetooth protocol.	5

ICE 443 Page 1 of 1