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VI SEMESTER B.Tech (BME) DEGREE MAKE-UP EXAMINATIONS, JUNE 2017 SUBJECT: Microcontrollers (BME-304)

(REVISED CREDIT SYSTEM)

Tuesday, 13th June 2017: 2.00 p.m. - 5.00 p.m.
TIME: 3 HOURS

MAX. MARKS: 100

Instruction to Candidates: 1. Answer any FIVE full questions. 2. Assume relevant data if missing. 3. Give diagrams wherever necessary. Draw the format of the 8051 registers IE and IP, and write significance of each (a) (12)bit of the registers. How do you implement a time delay of 1 mS using the Timer 0 of the 8051 (8)microcontroller? Explain. How does the register PSW of the 8051 helps in selecting register banks? 2. (a) (6)Explain. What are the different operating modes of the 8051 timers? How do you (6)configure the mode? Draw the format of the register CCR of the 68HC11 and write the significance (8)of each bit. How do you interface an 8-channel, 8-bit ADC to the 8051 microcontroller? 3. (a) (12)Write an assembly language program to digitize an analog signal of frequency 100 Hz applied to channel-1 of the ADC. How do you make use of the UART of the 8051 in mode-1? Explain. (b) (8)Write an 8051 assembly language program to add ten 2-digit decimal numbers (a) (7) available in the internal data memory. Store the result in the registers R0 and R1 from the default register-bank. What is the significance of initializing the stack pointer in the 8051 (5) microcontroller? Justify your answer with an illustration.

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	(c)	List and explain the unconditional branching instructions of the 8051 microcontroller.	(8)	
5.	(a)	Draw and explain basic topologies of SPI sub-system of the 68HC11 microcontroller.	(8)	
	(b)	Generate a square wave of frequency 1 KHz with a duty cycle of 50% on P0.1 pin of the microcontroller.	(8)	
	(c)	Write the features of PIC microcontroller.	(4)	
6. (a)		Explain in detail the following instructions of the 8051 microcontroller:		
		(i) MUL AB		
		(ii) DIV AB		
		(iii) ANL C, /bit		
		(iv) JNB bit, rel		
	(b)	Explain with an example to each, the following addressing modes of the 68HC11 microcontroller:	(8)	
		(i) Direct		
		(ii) Extended		
		(iii) Indexed		
		(iv) Inherent		

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