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## VI SEMESTER B.TECH (ELECTRICAL & ELECTRONICS ENGINEERING) MAKE-UP EXAMINATIONS, JUNE 2019

**SUBJECT: POWER ELECTRONICS [ELE 3201]** 

REVISED CREDIT SYSTEM

	REVISED CREDIT SYSTEM						
Time: 3 Hours Date: 10 June 2019 Max. Marks:							
Instr	ructions to Candidates:						
	Answer <b>ALL</b> the questions.						
	Missing data may be suitably assumed.						
1A.	Sketch the safe operating area of power MOSFET clearly indicating all the operating limits	(02)					
1B.	Draw a neat circuit for forced voltage commutation of an SCR. Hence, plot the waveforms voltage across the capacitor, voltage across the SCR and voltage across the load.	for <b>(04)</b>					
<b>1C.</b>	With the help of a neat sketch of Triac's structure, explain its working in first quadrant wheturned on with positive gate current.	nen <b>(04)</b>					
2A.	A single phase to single phase Cycloconverter is delivering power to a resistive load .1 frequency ratio fo/fs=1/3. The firing delay angle $\alpha$ for all the thyristors are the same. Ske the output voltage waveform in synchronization with input voltage for $\alpha$ =45°.						
2B.	A controlled half-wave rectifier has an ac input of 120 V rms at 60 Hz, R = 2 $\Omega$ , L = 20 mH a V <sub>dc</sub> = 100 V. The delay angle is 45°. Determine an expression for the output current and her find the rms value of the current.						
2C.	A controlled single phase bridge rectifier has $20~\Omega$ resistive load and has a $120~V$ rms, $60~$ ac source. The delay angle is $45^\circ$ . Determine average load current, rms load current and input power factor.						
3A.	A buck converter has the following parameters: $V_s$ = 15 V, D = 0.6, L = 10 $\mu$ H, C = 50 $\mu$ F a R = 5 $\Omega$ . The switching frequency is 150 kHz. Determine output voltage, maximum a minimum inductor currents.						
3B.	A boost converter has an input of 5 V and an output of 25 W at 15 V. The minimum induction that the no less than 50 percent of the average. The output voltage ripple must be 1 than 1 percent. The switching frequency is 300 kHz. Determine the duty ratio, inductor a capacitor values.	ess					
3C.	Discuss the differences between voltage source inverters and current source inverters	(02)					
4A.	With the help of neat circuit schematic and triggering sequence, plot any two phase volta waveforms and corresponding line voltage waveform for a three phase square wave brid inverter when conduction angle of each device is 180°.						

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- **4B.** With a neat circuit schematic, explain the working of a single phase full bridge square wave inverter. Hence, plot the output voltage waveform in synchronization with the switching scheme. Also, draw the harmonic spectrum. (05)
- **5A.** Discuss how unipolar switching technique can be applied to a single phase full bridge inverter. Support your answer with relevant waveforms (05)
- **5B.** With the help of neat circuit schematic, draw the voltage across the resonant capacitor and the current through the resonant inductor for a zero voltage switched buck converter. (05)

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