

Reg.No.



MANIPAL INSTITUTE OF TECHNOLOGY

MANIPAL

(A constituent unit of MAHE, Manipal)

IV SEMESTER B.TECH. (COMPUTER SCIENCE & ENGINEERING) END SEMESTER MAKEUP EXAMINATION, MAY 2023

SUBJECT: OPERATING SYSTEMS [CSE 2272]
REVISED CREDIT SYSTEM

Time: 3 Hours

MAX. MARKS: 50M

Instructions to Candidates:

- ❖ Answer **ALL** the questions.
- ❖ Missing data may be suitably assumed.

1A.	Write bankers algorithm for dead lock avoidance. Considering a system with five processes P0 through P4 and three resource types A, B, C is given below. i) Calculate the need matrix. ii) Check whether the system is safe or not? If safe, what is the safe sequence? iii) Calculate the total sum of each type of resource?	(5M) (CO3, L2,L3)																																																																					
<table><tr><th rowspan="2">Processes</th><th colspan="3">Allocation</th><th colspan="3">Max</th><th colspan="3">Available</th></tr><tr><th>A</th><th>B</th><th>C</th><th>A</th><th>B</th><th>C</th><th>A</th><th>B</th><th>C</th></tr><tr><td>P0</td><td>1</td><td>1</td><td>2</td><td>4</td><td>3</td><td>3</td><td>2</td><td>1</td><td>0</td></tr><tr><td>P1</td><td>2</td><td>1</td><td>2</td><td>3</td><td>2</td><td>2</td><td></td><td></td><td></td></tr><tr><td>P2</td><td>4</td><td>0</td><td>1</td><td>9</td><td>0</td><td>2</td><td></td><td></td><td></td></tr><tr><td>P3</td><td>0</td><td>2</td><td>0</td><td>7</td><td>5</td><td>3</td><td></td><td></td><td></td></tr><tr><td>P4</td><td>1</td><td>1</td><td>2</td><td>1</td><td>1</td><td>2</td><td></td><td></td><td></td></tr></table> <p>Table Q.1A</p>			Processes	Allocation			Max			Available			A	B	C	A	B	C	A	B	C	P0	1	1	2	4	3	3	2	1	0	P1	2	1	2	3	2	2				P2	4	0	1	9	0	2				P3	0	2	0	7	5	3				P4	1	1	2	1	1	2			
Processes	Allocation			Max			Available																																																																
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P3	0	2	0	7	5	3																																																																	
P4	1	1	2	1	1	2																																																																	
1B.	Describe how the Swap() instruction can be used to provide mutual exclusion that satisfies the bounded waiting requirement.	(3M) (CO2, L2)																																																																					
1C.	Consider process P ₀ is interrupted during its execution and there is a context-switch to another Process P ₁ by the CPU. Describe the action taken by a kernel to context-switch between the processes with a diagram.	(2M) (CO1, L2)																																																																					
2A.	Explain segmentation architecture. How is protection achieved in segmentation? Consider five memory partitions of size 100 KB, 500 KB, 200 KB, 450 KB and 600 KB in same order. If sequence of requests for blocks of size 212 KB, 417 KB, 112 KB and 426 KB in same order come, Apply First fit, worst fit and best fit algorithms for memory allocations. Find the optimal algorithm for memory allocation and justify.	(5M) (CO4, L2,L3)																																																																					

2B.	<p>i. List the roles of Lazy swapper.</p> <p>ii. Calculate Average waiting time and Average turnaround time for non-preemptive SJF and preemptive SJF with the help of a Gantt Chart for the following data.</p> <table border="1"> <thead> <tr> <th>Process</th><th>Arrival Time (msec)</th><th>Burst Time(msec)</th></tr> </thead> <tbody> <tr> <td>P1</td><td>0</td><td>8</td></tr> <tr> <td>P2</td><td>4</td><td>5</td></tr> <tr> <td>P3</td><td>3</td><td>2</td></tr> <tr> <td>P4</td><td>2</td><td>1</td></tr> </tbody> </table> <p style="text-align: center;">Table Q.2B</p>	Process	Arrival Time (msec)	Burst Time(msec)	P1	0	8	P2	4	5	P3	3	2	P4	2	1	(3M) (CO4, CO3)(L2,L3)
Process	Arrival Time (msec)	Burst Time(msec)															
P1	0	8															
P2	4	5															
P3	3	2															
P4	2	1															
2C.	Explain the microkernel operating system structure with diagram, benefits, and detriments.	(2M) (CO1, L2)															
3A	Apply FIFO, LRU and optimal page replacement algorithms for the following page reference string. Page reference string 7,0,1,2,0,3,0,4,2,3,0,3,2,1,2,0,1,7,0,1. Assume 3 frames and all frames are initially empty. Calculate the number of page faults for each algorithm.	(5M) (CO4,L3)															
3B	Explain the handling of a user application invoking the open () system call with a neat diagram and state the purpose of medium-term scheduler with neat diagram.	(3M) (CO1,L2)															
3C	Explain in detail about threading and its models with neat diagram. Explain the challenges in multicore programming.	(2M) (CO1,L2)															
4A	Explain in detail about page table structure and its variation with suitable example. Define fragmentation and suggest the solution to reduce external fragmentation.	(5M) (CO4,L2)															
4B	Let the page fault service time be 10ms in a computer with average memory access time being 20ns. If one page fault is generated for every 10^6 memory accesses, what is the effective access time for the memory? List steps involved in page fault service.	(3M) (CO4,L3)															
4C	Explain sequential and direct access methods of a file with examples.	(2M) (CO5,L2)															
5A	Assume that a disk drive has cylinders numbered 0 to 4999. Cylinder 143 is currently serving by drive, and the cylinder 125 was a previous request. The 86, 1470, 913, 1774, 948, 1509, 1022, 1750, 130 are pending requests. Suggest any two best disk-scheduling algorithms from FCFS, SSTF, SCAN, C-SCAN and C-LOOK, those uses minimum distance to satisfy all the pending requests. Justify your answer.	(5M) (CO5,L3)															
5B	Explain the any three ways of implementing the access matrix.	(3M) (CO5,L2)															
5C	Discuss the strengths and weaknesses of implementing an access matrix using capabilities that are associated with domains.	(2M) (CO5,L2)															