Question Paper

Exam Date & Time: 02-Dec-2023 (09:30 AM - 12:30 PM)



MANIPAL ACADEMY OF HIGHER EDUCATION

THIRD SEMESTER B.TECH END SEMESTER EXAMINATIONS, NOV/DEC 2023

ELECTRONICS CIRCUITS [BME 2122]

Analyze the amplifier circuit shown below for voltage gain and output resistance ignoring load

Marks: 50

1)

Answer all the questions.

Missing data may be suitably assumed

- B) Modify a common source JFET amplifier in to a sinusoidal oscillator using RC network, and explain (3) the operation of the oscillator.
- C) Develop a zero-cross detector using an Op-amp and explain its operation.
- 2) Develop a negative edge triggered monostable multivibrator using an Op-amp and derive an (4) expression for pulse width.

(4)

Duration: 180 mins.

(3)

- A)
- B) How do you make use of an Op-amp for generating timing marker signals? Explain.
- C) For the circuit shown below, compute
 - (i) The time constant required to produce an output of frequency 2 KHz.
 - (ii) The value of R required when C= 0.01μ F.



3) How do you employ successive approximation technique and a DAC to convert an analog signal in (4) to digital signal? Explain.

A)

B)	Design an 8-bit R-2R ladder DAC such that the full-scale output is 10V. Assume a reference	(3)
	voltage of 5V. Draw the circuit diagram for the designed DAC.	

- C) Modify a 555 timer astable multivibrator designed to produce a square wave output of 1 KHz with (3) 50% duty cycle in to a voltage controlled oscillator. Draw the necessary waveforms. And write where you can make use of your design?
- A medical device requires a regulated power supply with the following specifications. (5)
 (i) V_O = 10V.
 (ii) Load current = 400 mA. Design a regulated power supply using IC7805 for the above specifications. Assume a quiescent current of 4 mA.
 B) How do you make use of a 555 timer astable mutlivibrator as a Schmitt trigger? Explain in detail. (3)
 - C) Construct a -5V regulator using a suitable regulator IC and explain its operation. (2)
- 5) Design a IC 555 timer based astable multivibrator to generate an output of frequency 1 KHz with (4) 75% duty cycle and amplitude 5V. Assume a capacitor of value 0.1μ F.

A)

- B) How do you make use of IC 555 timer astable mutlivibrator to generate free-running ramp signal? (4)
 Explain.
- C) Develop a small signal a.c equivalent circuit for the amplifier circuit shown below. (2)

(3) (3)



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