Question Paper

Exam Date & Time: 09-May-2024 (02:30 PM - 05:30 PM)



MANIPAL ACADEMY OF HIGHER EDUCATION

FOURTH SEMESTER B.TECH. (ELECTRONICS AND COMMUNICATION ENGINEERING) DEGREE EXAMINATIONS -APRIL / MAY 2024 SUBJECT: ECE 2225/ECE 2225 - MODERN CONTROL THEORY

Marks: 50

Duration: 180 mins.

Answer all the questions.

Missing data may be suitably assumed.

1A)

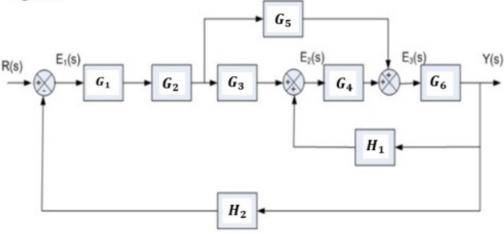
The open loop transfer function of a unity feedback system is given by (5)

$$G(s) = \frac{K}{(s+2)(s^2+2s+2)}.$$
Sketch the root locus of the system.

1B)

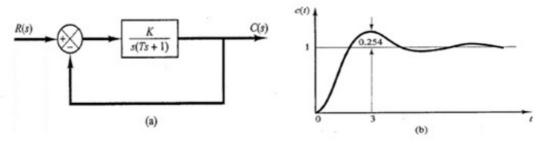
Modify the block diagram shown in Fig.Q1B and develop the closed loop transfer function using (3) block diagram reduction techniques.

Fig.Q1B



1C)

When the system shown in Fig. Q1C. (a) is subjected to a unit-step input, the system output (2) responds as shown in Fig.Q1C (b). Determine the values of K and T from the response curve.

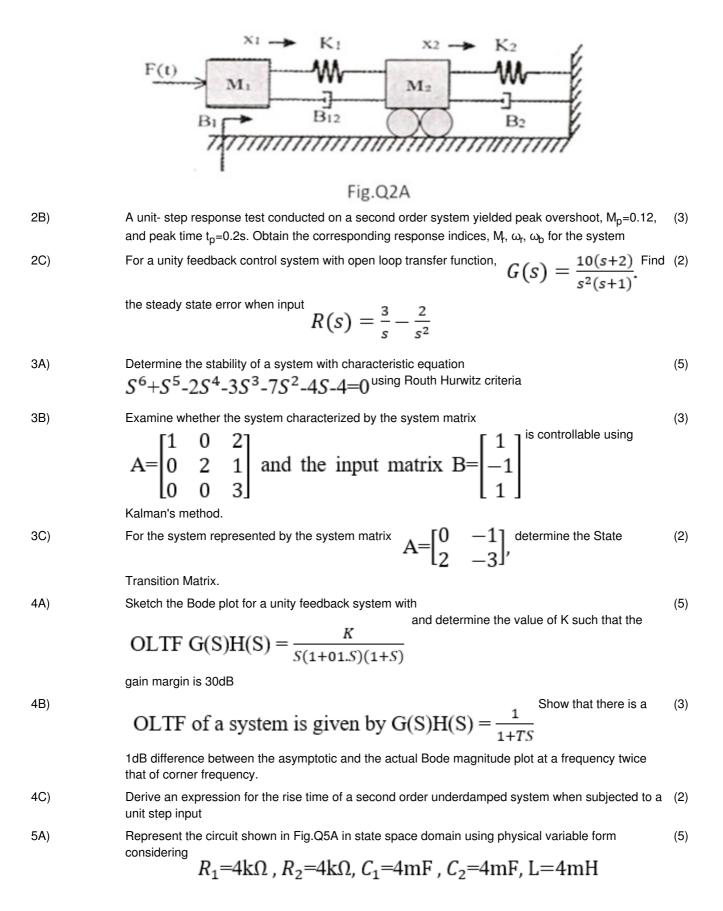




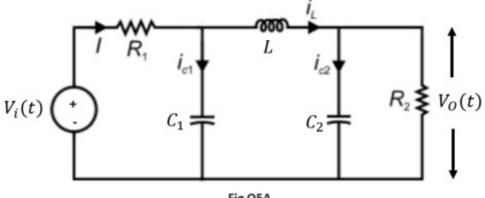
(5)

Identify the differential equations governing the mechanical behaviour of the system shown in Fig.Q2A. Draw the FI and FV analogous circuits along with mesh and node equations.

2A)



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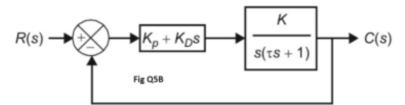


5B)

5C)

A PD controller is cascaded to the forward path of a unity feedback system with plant transfer (3) function as shown below. Derive an expression for steady state error $G_P(S) = \frac{K}{S(1+\tau S)}$

when unit ramp input is applied. Also, express damping ratio in terms of the system parameters.

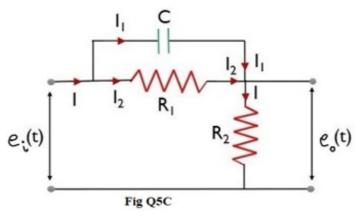


Circuit of a Phase-Lead Compensator is shown in

(2)

Identify the Fig. Q5C where $R_1 = 2k\Omega R_2 = 3k\Omega$ and C= 100µF.

frequency where the maximum phase lead occurs. Also, calculate the max phase angle contributed by the Lead network.



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